

# Borrowing memory that's being used: catalytic approaches to the Tree Evaluation Problem

*James Cook, Ian Mertz*

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## The Tree Evaluation Problem

Motivation and definition

Branching programs and pebbling games

Lower bounds

## New algorithm

Reversible computation

Solving TEP

# Section 1

## The Tree Evaluation Problem

Pebbles and Branching Programs for Tree Evaluation [S. Cook, P. McKenzie, D. Wehr, M. Braverman, R. Santhanam 2010]

New Results for Tree Evaluation [S. Chan, J. Cook, S. Cook, P. Nguyen, D. Wehr 2010]

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- Branching programs and pebbling games

- Lower bounds

### New algorithm

- Reversible computation

- Solving TEP

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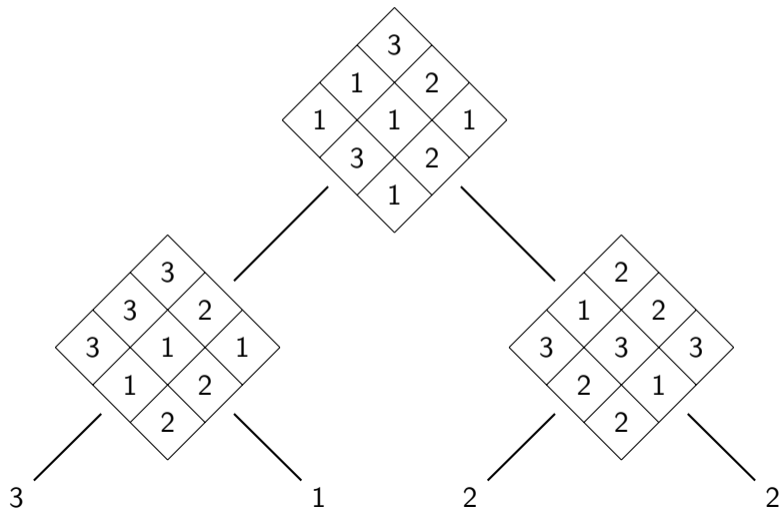
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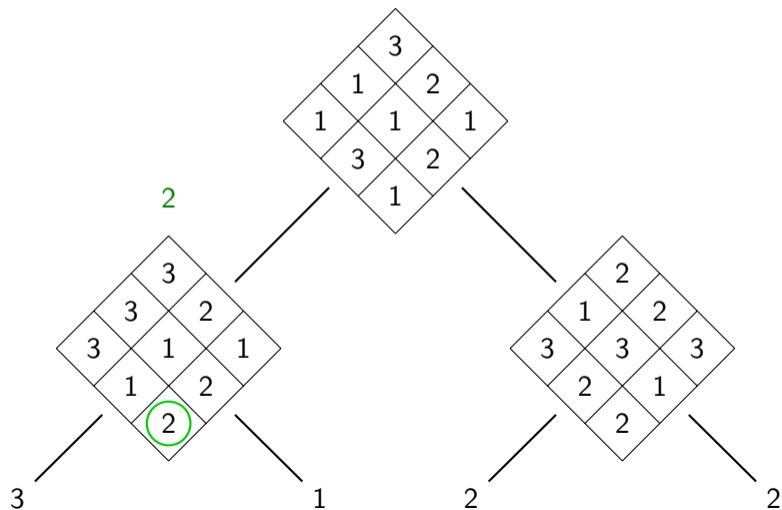
TEP  $\in$  P.

Goal: prove TEP  $\notin$  L, so  $L \neq P$ .

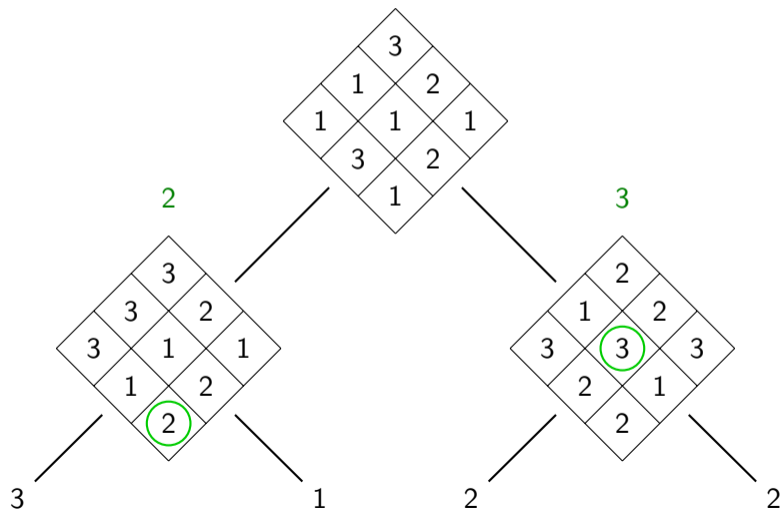
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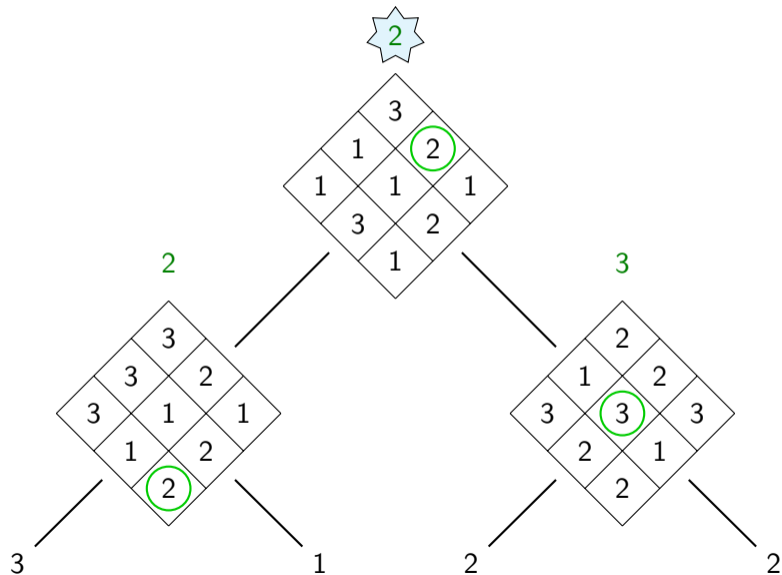
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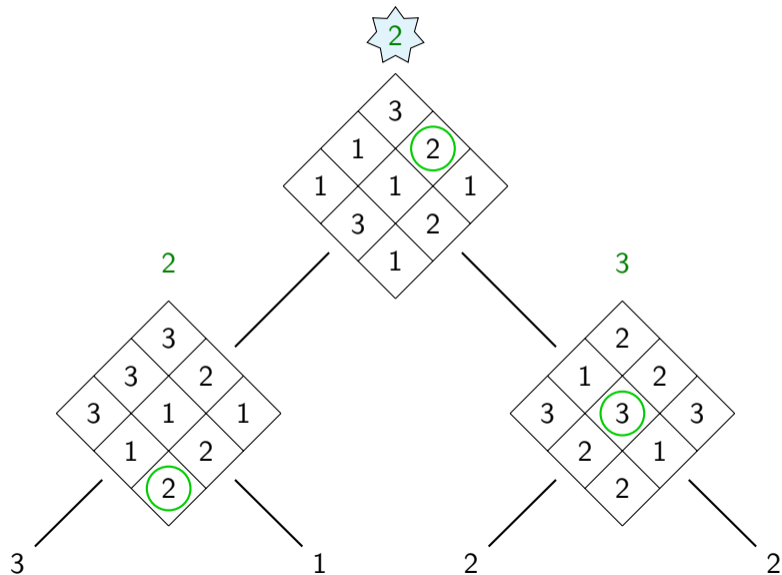
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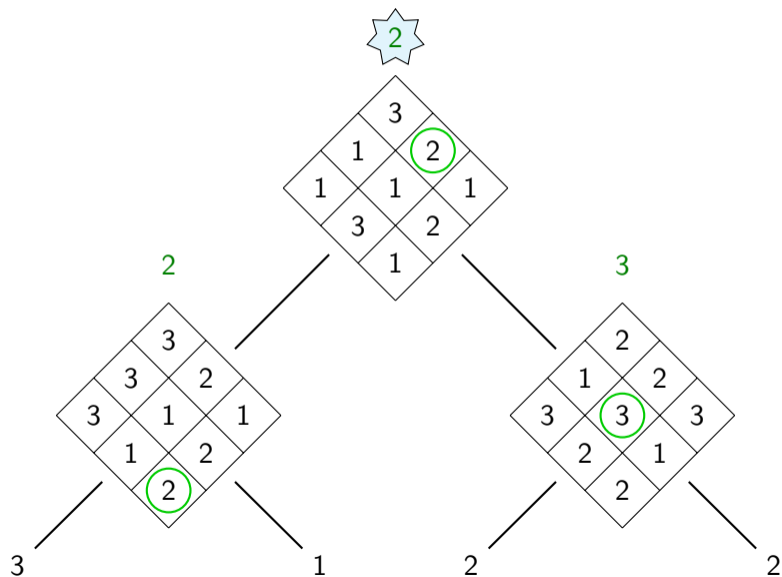
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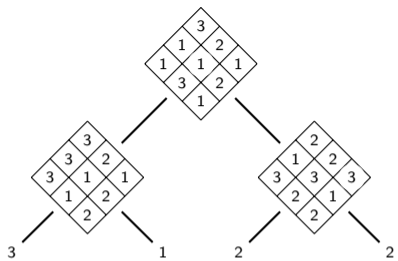
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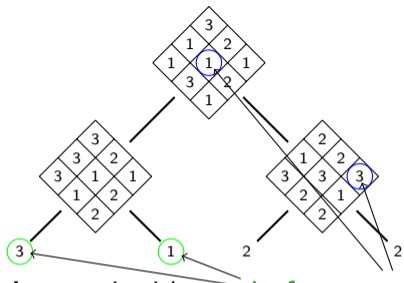
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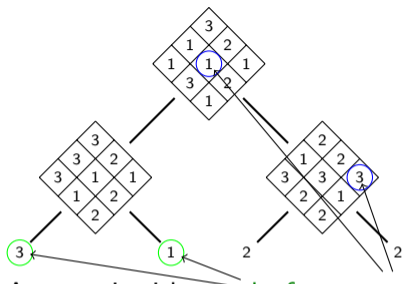
Solving TEP







A query is either a **leaf** or a **cell** in a table of an internal node.

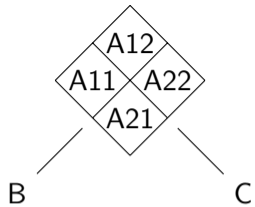


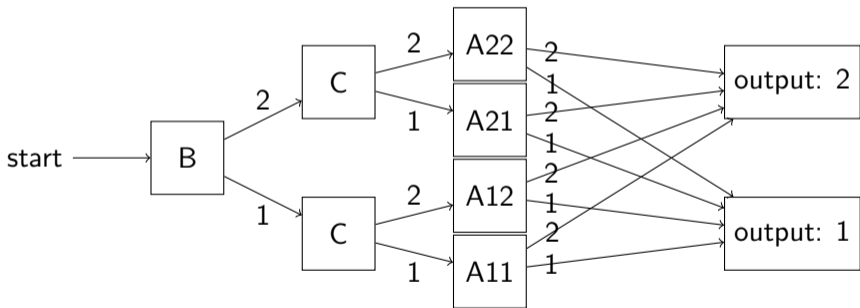
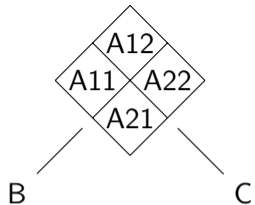
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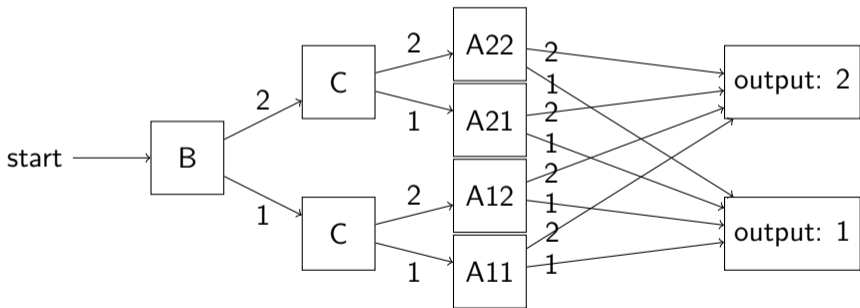
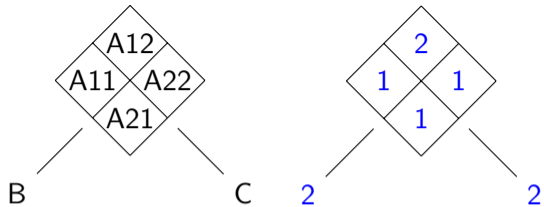
A *branching program* is a directed graph of *states*. There are two kinds of state:

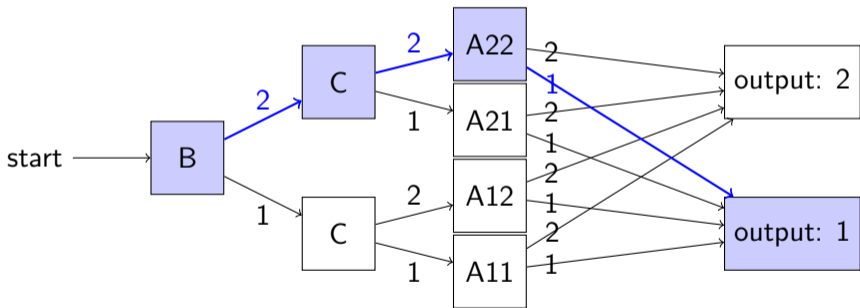
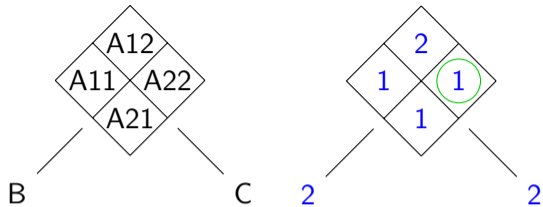
- ▶ *query state*: labelled with a query and has  $k$  outgoing edges labelled with the possible answers.
- ▶ *final state*: labelled with a number  $1..k$ .

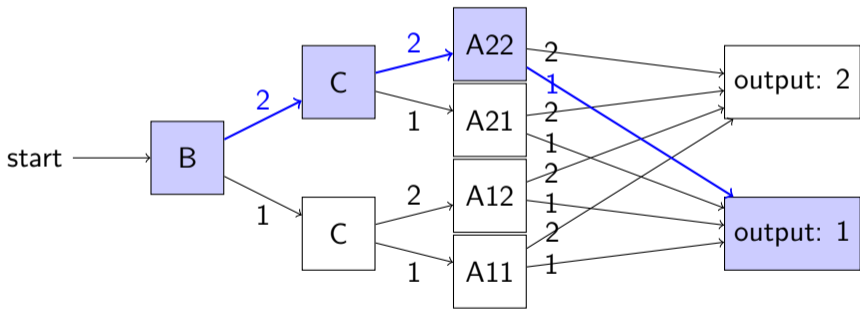
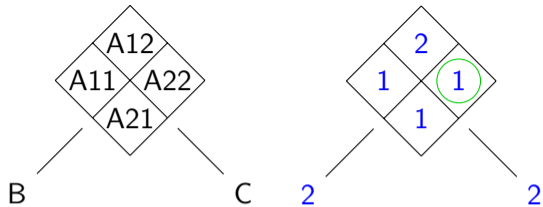
One state is the starting state.











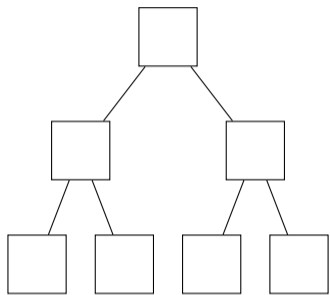
remember B

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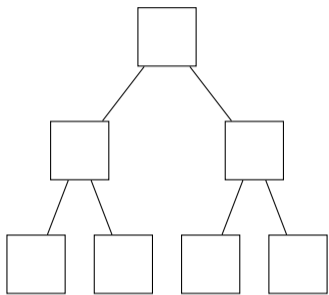
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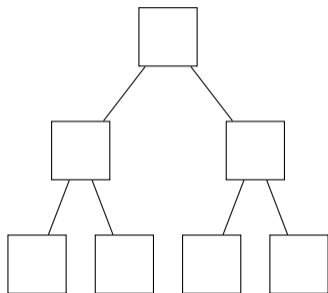


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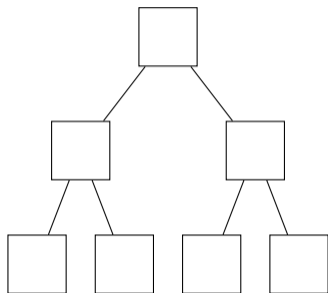


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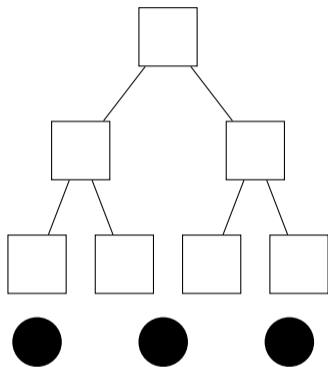
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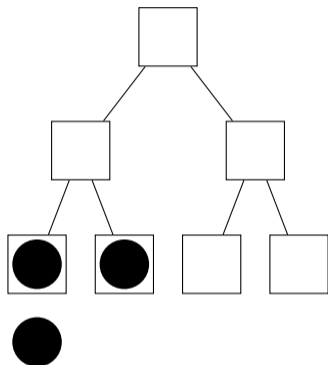
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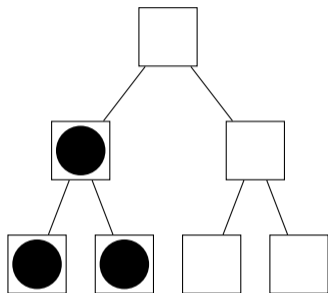
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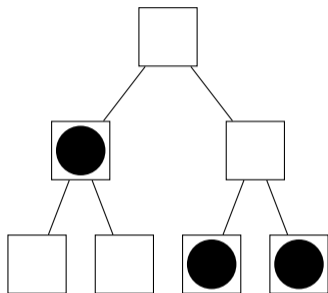
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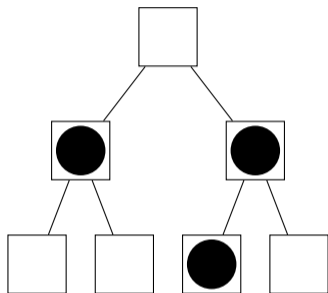
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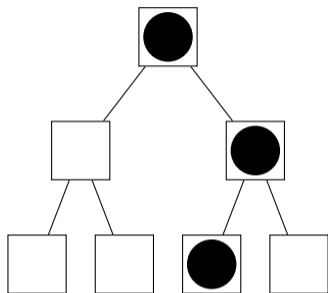
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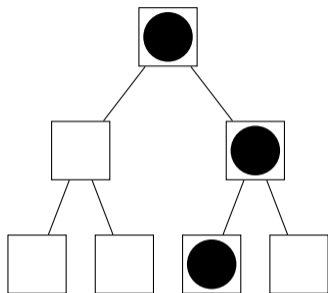
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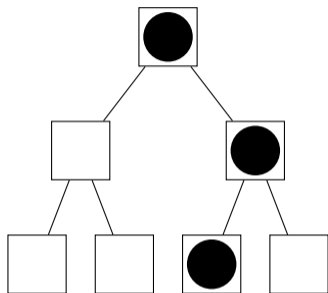
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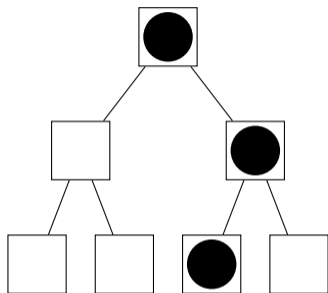
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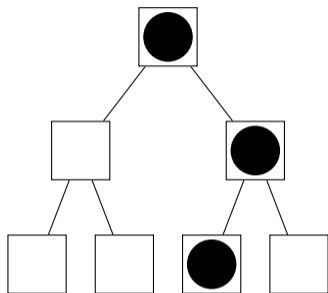
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Conjecture (false): To solve TEP, a branching program needs  $\Omega(k^h)$  states.

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**Lower bounds**

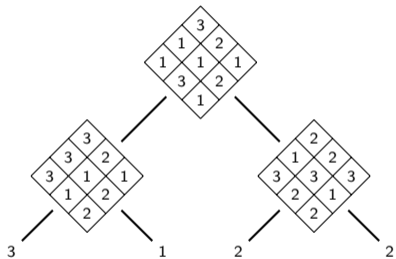
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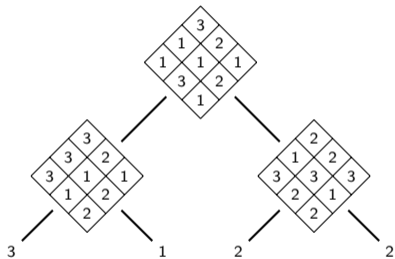
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Neither algorithm fits in  $2^{O(h)} k^{O(1)}$  states, so  $\text{TEP} \notin \text{L}$  is still possible.

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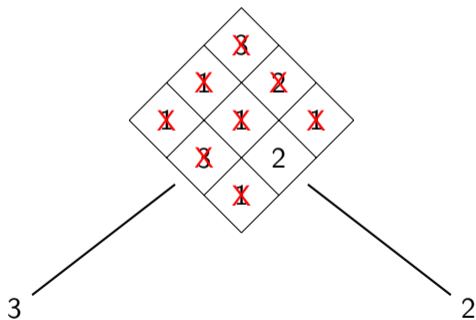
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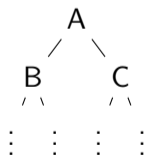
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This rules out the following lower bound argument:

- ▶ At some point, you need to compute B.
- ▶ You need to remember B ( $\log k$  bits) while computing C.
- ▶ So, every level of the tree adds  $\log k$  bits you need to remember.

*Bounded-width polynomial-size branching programs recognize exactly those languages in NC<sup>1</sup>.* [D. Barrington 1989]

*Computing algebraic formulas using a constant number of registers.* [M. Ben-Or, R. Cleve 1992]

Ring  $R$

Inputs  $x_1, \dots, x_n \in R$

Work registers  $r_1, \dots, r_m \in R$

Reversible instructions:

- ▶ Example:  $r_5 \leftarrow r_5 + r_4 \times x_1$ .
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A sequence of reversible instructions *cleanly computes*  $f$  into  $r_i$  if, once it finishes:

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Invert the whole sequence by running the inverse of each instruction in reverse order.  
(Computes  $-f$ .)

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## Lemma

Suppose there is a sequence of  $\ell$  instructions that cleanly computes  $f$ , and each instruction has the form:

$$(r_1, \dots, r_m) \leftarrow g(x_j, r_1, \dots, r_m)$$

Then there is a branching program that computes  $f$  with  $\ell|R|^m$  states.

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- ▶  $r_1 \leftarrow r_1 + x_2$       [ $r_1 = \tau_1 + x_1 + x_2$ ]

## Lemma: Multiplication

Suppose  $P_1$  cleanly computes  $f_1$  into  $r_1$  and  $P_2$  cleanly computes  $f_2$  into  $r_2$ . Then we can cleanly compute  $f_1 \times f_2$  into  $r_3$  as follows:



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Cost: need to run  $P_1$  and  $P_2$  twice each. But: no memory needs to be reserved.

## The Tree Evaluation Problem

Motivation and definition

Branching programs and pebbling games

Lower bounds

## New algorithm

Reversible computation

Solving TEP

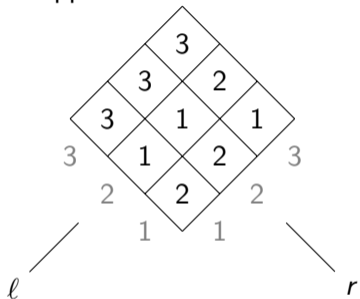
## A formula for TEP

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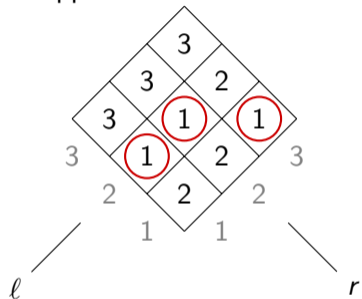


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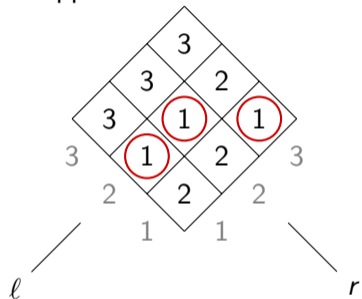


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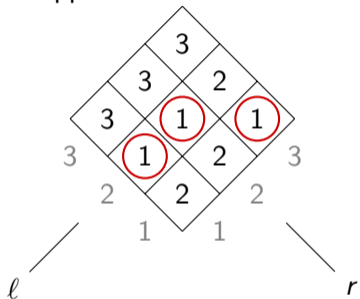
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$$[l = 2] \times [r = 1] + [l = 2] \times [r = 2] + [l = 1] \times [r = 3]$$

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$$[v = 1] = [l = 2] \times [r = 1] + [l = 2] \times [r = 2] + [l = 1] \times [r = 3]$$

Let  $f_v$  denote  $v$ 's table. In general,

$$[v = x] = \sum_{(y,z) \in [k]^2} [f_v(y,z) = x] \times [l = y] \times [r = z]$$

## First attempt

$$[v = x] = \sum_{(y,z) \in [k]^2} [f_v(y, z) = x] \times [\ell = y] \times [r = z]$$

### Algorithm

CheckNode( $v, x, i$ )

Parameters: node  $v$ , value  $x \in [k]$ , target register  $i$

Computes  $r_i \leftarrow r_i + [v = x]$



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using multiplication lemma: 2 calls each to CheckNode( $\ell, y, j$ ) and CheckNode( $r, z, j'$ ), where  $j$  and  $j'$  are two registers other than  $i$ .

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Needs three registers.

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Worse than pebbling, which uses  $\Theta((k+1)^h)$  states.

- ▶ for  $(y, z) \in [k]^2$ :
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$$r_i \leftarrow r_i - r_j \times r_{j'}$$

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...

...

...



## One-hot encoding

Given a value  $x \in [k]$ , define  $\text{OneHot}(x) = ([x = 1], [x = 2], \dots, [x = k]) \in \{0, 1\}^k$ .

E.g. for  $k = 3$ ,  $\text{OneHot}(2) = (0, 1, 0)$ .

## Algorithm

ComputeOneHot( $v, i$ )      Uses *vector registers*  $\vec{r}_i \in \{0, 1\}^k$ .

Parameters: node  $v$ , target register  $i$

Computes  $\vec{r}_i \leftarrow \vec{r}_i + \text{OneHot}(v)$

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  - ▶  $\vec{r}_j \leftarrow \vec{r}_j + \text{OneHot}(\ell)$
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$$F(\vec{r}_j, \vec{r}_{j'})_x = \sum_{(y,z) \in [k]^2} [f_v(y, z) = x] \times (\vec{r}_j)_y \times (\vec{r}_{j'})_z$$

Note:

$$[v = x] = \sum_{(y,z) \in [k]^2} [f_v(y, z) = x] \times [\ell = y] \times [r = z]$$

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$$F(\vec{r}_j, \vec{r}_{j'})_x = \sum_{(y,z) \in [k]^2} [f_v(y,z) = x] \times (\vec{r}_j)_y \times (\vec{r}_{j'})_z$$

Note:

$$[v = x] = \sum_{(y,z) \in [k]^2} [f_v(y,z) = x] \times [\ell = y] \times [r = z]$$

Gives branching program with width  $2^{3k}$ , length  $\Theta(k^2 4^h)$ . Total  $2^{\Theta(k+h)}$  states.

Pebbling algorithm:  $\Theta((k + 1)^h)$   
ComputeOneHot:  $2^{\Theta(k+h)}$  states.

Pebbling algorithm:  $\Theta((k+1)^h) = \Theta(2^{h \log_2(k+1)})$

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Can we do better?

## Binary encoding

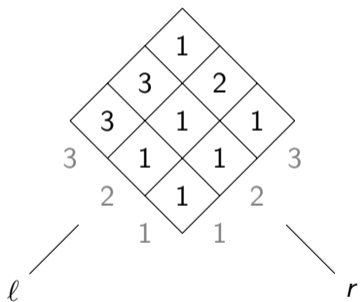
Given a value  $x \in [k]$ , let  $\text{Bin}(x) \in \{0, 1\}^{\lceil \log k \rceil}$  be its binary encoding.

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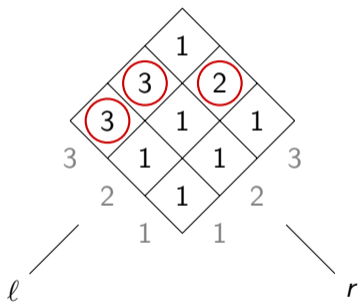


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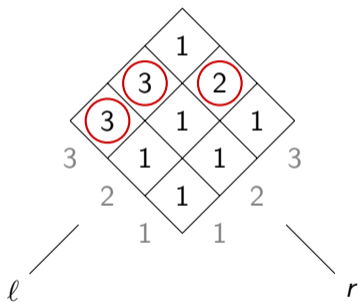


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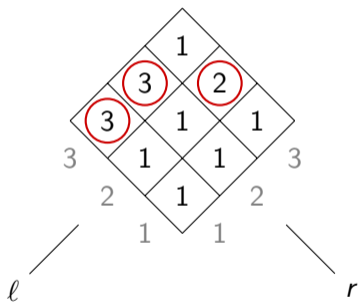


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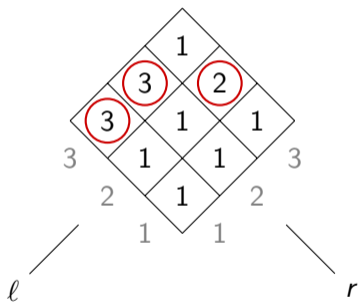
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$$[l = 1] = (1 + \text{Bin}(l)_1) \times \text{Bin}(l)_2$$

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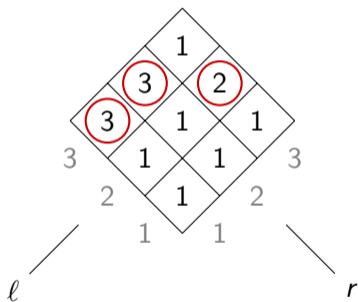
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In general,  $\text{Bin}(v)_x$  can be written as a degree- $2\lceil \log k \rceil$  polynomial involving  $\text{Bin}(\ell)$  and  $\text{Bin}(r)$ .



## Lemma: Multiplication

Suppose  $P_1$  cleanly computes  $f_1$  into  $r_1$  and  $P_2$  cleanly computes  $f_2$  into  $r_2$ . Then we can cleanly compute  $f_1 \times f_2$  into  $r_3$  as follows:

- ▶  $P_1$
- ▶  $r_3 \leftarrow r_3 - r_1 \times r_2$
- ▶  $P_2$
- ▶  $r_3 \leftarrow r_3 + r_1 \times r_2$
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- ▶  $P_2$        $[r_1 = \tau_1 + f_1, r_2 = \tau_2 + f_2]$
- ▶  $r_3 \leftarrow r_3 + r_1 \times r_2$
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- ▶  $r_3 \leftarrow r_3 - r_1 \times r_2$
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## Lemma: $d$ -ary multiplication

Suppose we have  $d$  values  $f_1, \dots, f_d$ , and a general subroutine  $P$ . For any  $S \subseteq [d]$ ,  $P(S)$  cleanly computes  $r_i \leftarrow r_i + f_i$  for every  $i \in S$ , and leaves  $r_j$  alone for  $j \notin S$ . Then we can cleanly compute  $f_1 \times \dots \times f_d$  into  $r_{d+1}$  as follows:

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  - ▶  $r_{d+1} \leftarrow r_{d+1} + c_S \times \prod_{i=1}^d r_i$
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Uses  $d + 1$  registers and  $2^d$  recursive calls.

## Algorithm

ComputeBin( $v, S, i$ )      Uses *vector registers*  $\vec{r}_i \in \{0, 1\}^{\lceil \log k \rceil}$ .

Parameters: node  $v$ , set  $S \subseteq [\log k]$ , target register  $i$

Computes:  $\vec{r}_{i_b} \leftarrow \vec{r}_{i_b} + \text{Bin}(v)_b$  for all  $b \in S$

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Worse than pebbling, which uses  $\Theta((k+1)^h)$  states.

algorithm	width	length	total states
One-hot	$2^{\Theta(k)}$	$\Theta(k^2 4^h)$	$2^{\Theta(k+h)}$
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Hybrid	$2^{\Theta(\frac{a}{2^a-1}k)}$	$2^{\Theta(ah)} k^{\Theta(1)}$	$2^{\Theta(ah + \frac{a}{2^a-1}k)}$

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Hybrid, $a = \log(\frac{k}{h} + 1)$	$\Theta((\frac{k}{h} + 1)^{\Theta(h)})$	$\Theta((\frac{k}{h} + 1)^{\Theta(h)})$	$(\frac{k}{h} + 1)^{5h} k^{\Theta(1)}$



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Hybrid, $a = \log(\frac{k}{h} + 1)$	$\Theta((\frac{k}{h} + 1)^{\Theta(h)})$	$\Theta((\frac{k}{h} + 1)^{\Theta(h)})$	$(\frac{k}{h} + 1)^{5h} k^{\Theta(1)}$

Pebbling uses  $\Theta((k + 1)^h)$  states. Hybrid is better when  $h = \omega(k^{4/5})$ .

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For example, with  $k = 9, a = 2$ :

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1	01	00	00	010000
2	10	00	00	100000
3	11	00	00	110000
4	00	01	00	000100
5	00	10	00	001000
6	00	11	00	001100
7	00	00	01	000001
8	00	00	10	000010
9	00	00	11	000011

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Using this, we can build an algorithm that uses 3 registers with  $\frac{ka}{2^a-1}$  bits each and makes  $2^{\Theta(a)}$  recursive calls at each level, for a total of  $2^{\Theta(ah)} k^{\Theta(1)}$  layers.

## Future work

- ▶ Improve the algorithm. (Better ways to compute  $d$ -ary products? We're not the first to want them.)
- ▶ Find new TEP lower bounds that apply to these algorithms. (Old lower bounds apply only to read-once or “thrifty” algorithms.)